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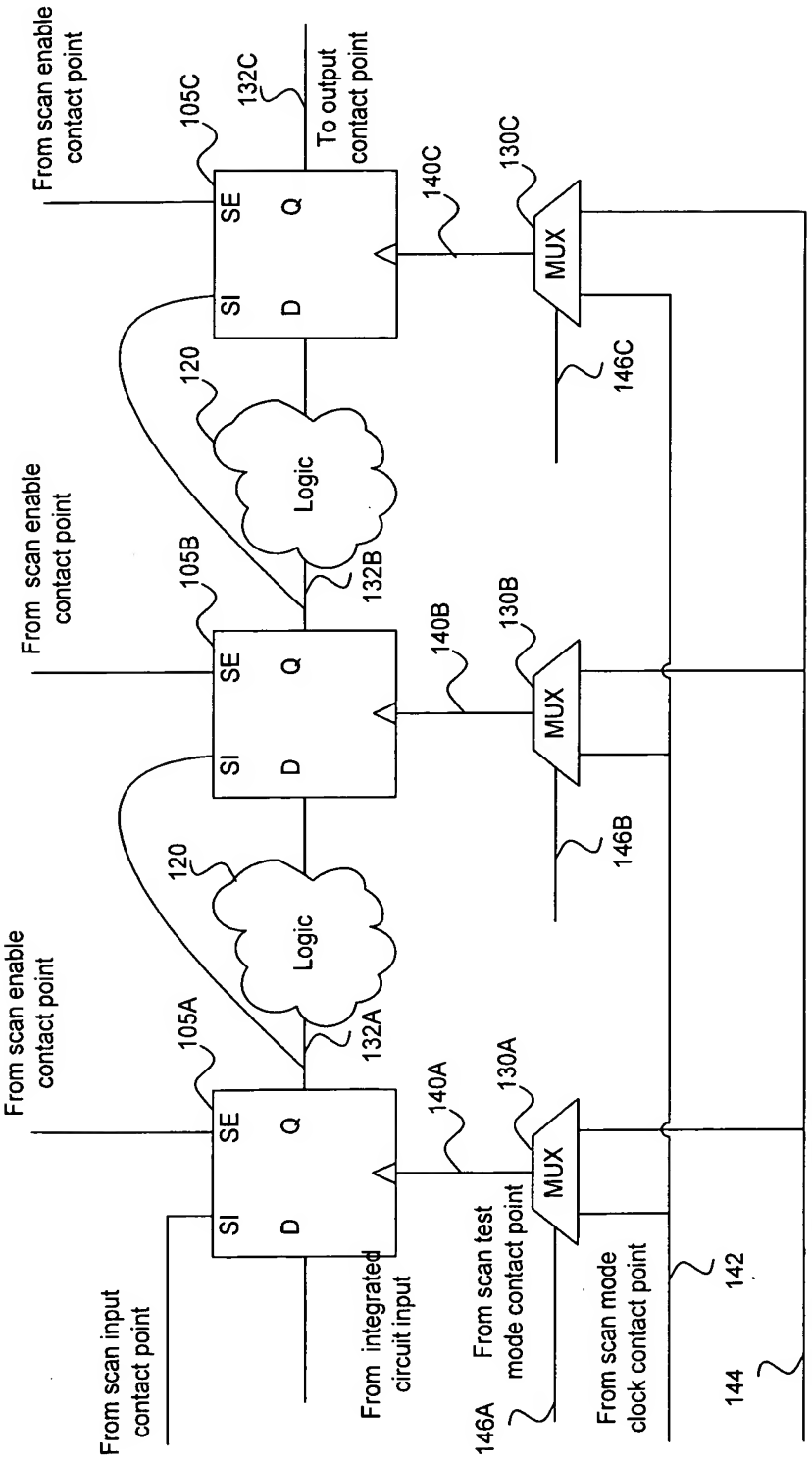


FIG. 1

0305-24.vsd/1

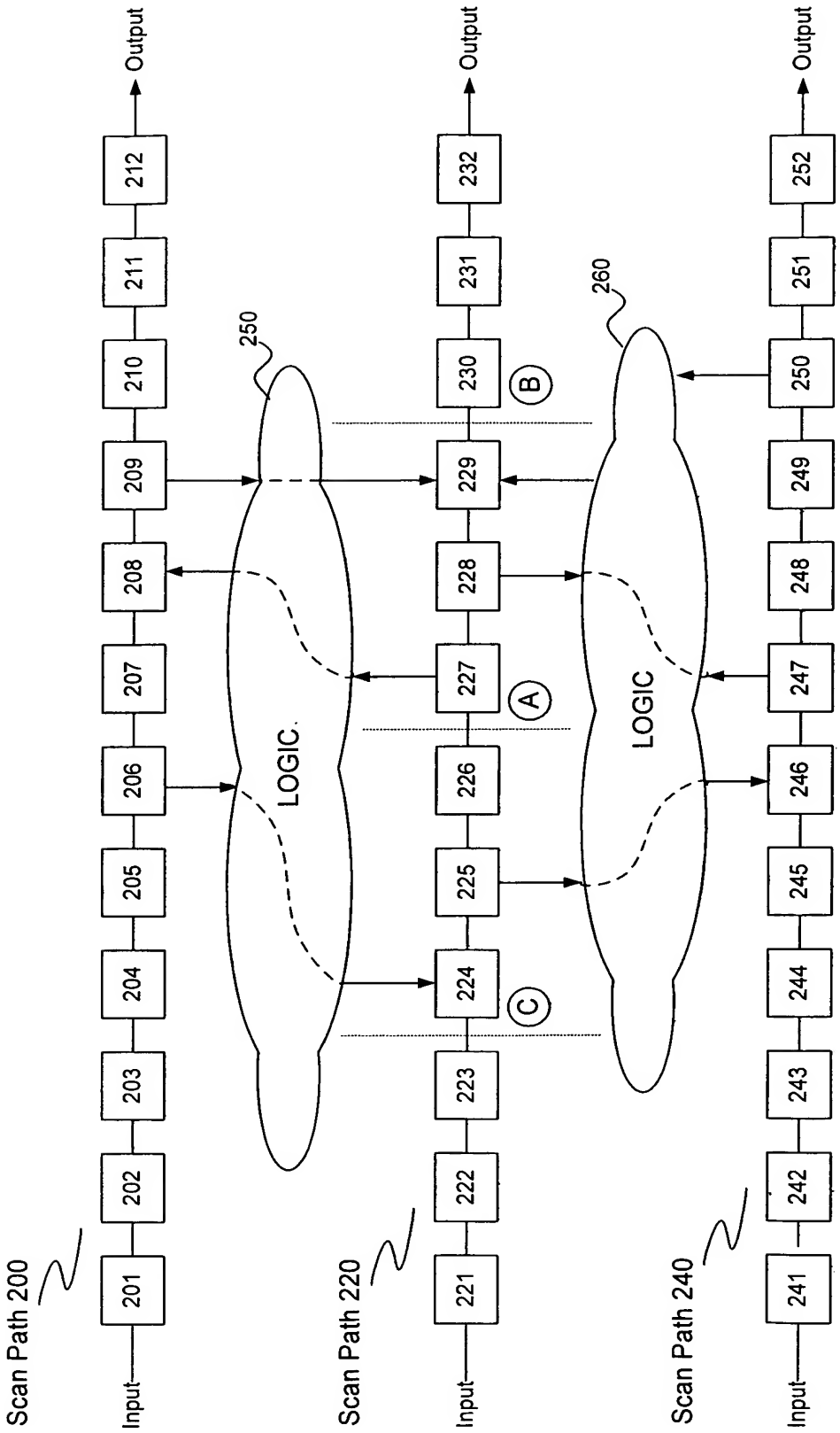
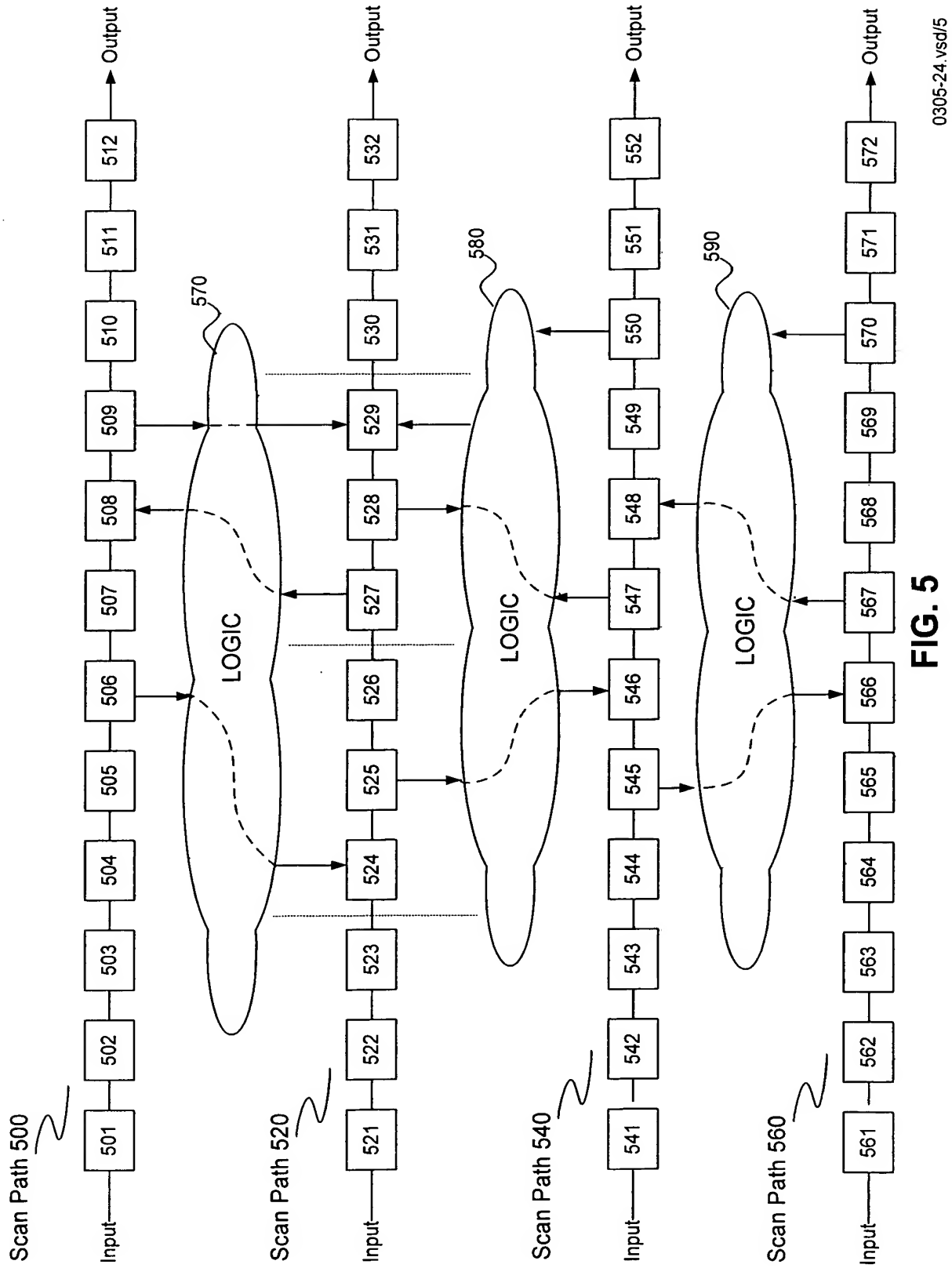


FIG. 2

0305-24.vsd/2


Scan Path 500
Scan Path 520
Scan Path 540
Scan Path 560
Input
Output
LOGIC
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0305-24.vsd/5

FIG. 5